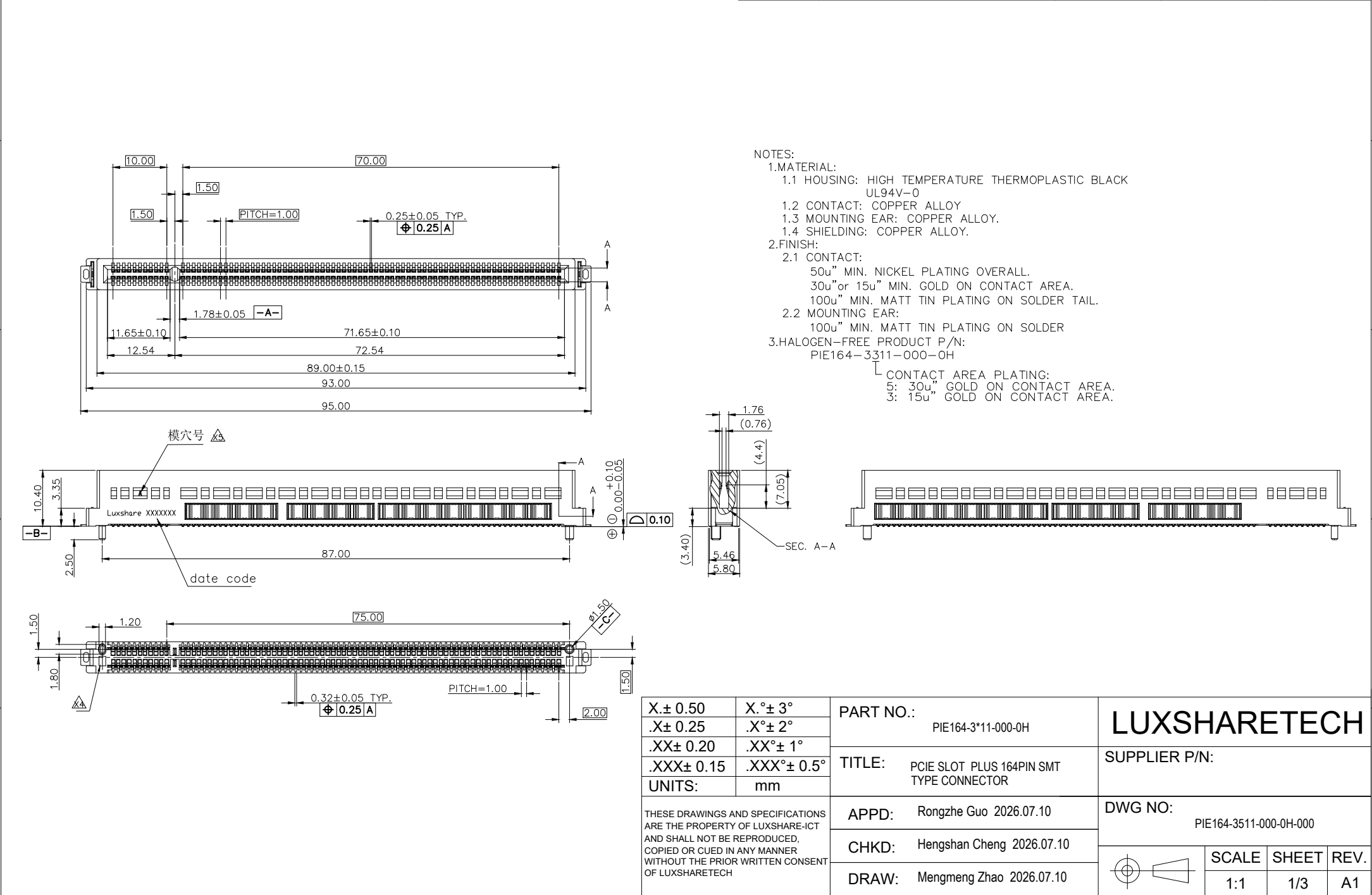
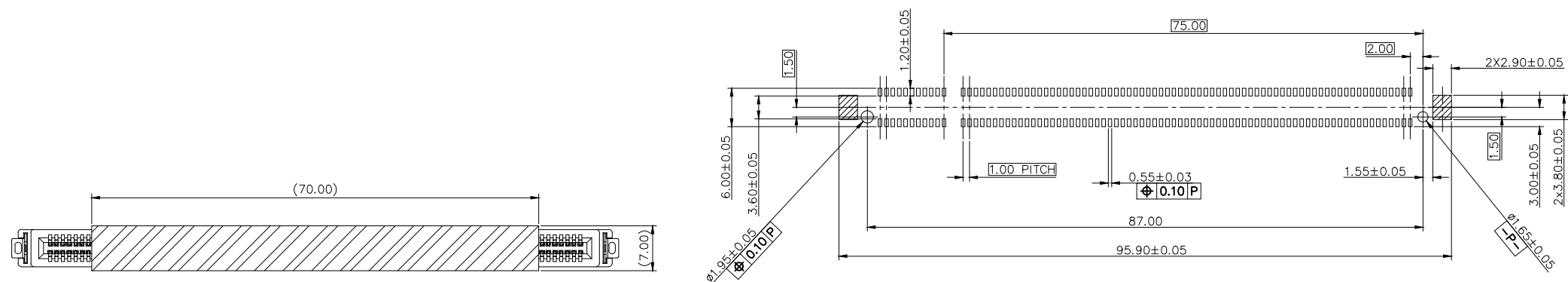
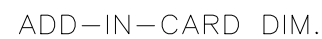
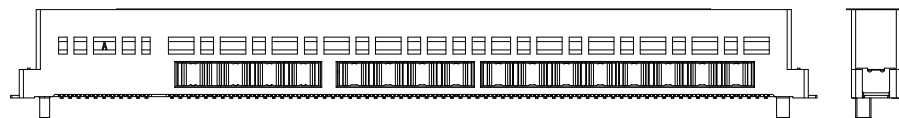


8	7	6	5	4	3	2	1	
CUSTOMER DRAWING				REV.	DESCRIPTION	DESIGN	APPROVAL	DATE
				A1	INITIAL RELEASE	Mengmeng Zhao	Rongzhe Guo	2026.07.10





RECOMMENDED PCB LAYOUT



POSITION OF MYLAR

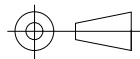
X.± 0.50	X.°± 3°	PART NO.: PIE164-3*11-000-0H	LUXSHARETECH			
.X± 0.25	.X°± 2°					
.XX± 0.20	.XX°± 1°					
.XXX± 0.15	.XXX°± 0.5°	TITLE: PCIE SLOT PLUS 164PIN SMT TYPE CONNECTOR	SUPPLIER P/N:			
UNITS:	mm					
THESE DRAWINGS AND SPECIFICATIONS ARE THE PROPERTY OF LUXSHARE-ICT AND SHALL NOT BE REPRODUCED, COPIED OR CUED IN ANY MANNER WITHOUT THE PRIOR WRITTEN CONSENT OF LUXSHARETECH		APPD: Rongzhe Guo 2026.07.10	DWG NO: PIE164-3511-000-0H-000			
		CHKD: Hengshan Cheng 2026.07.10				
		DRAW: Mengmeng Zhao 2026.07.10		SCALE	SHEET	REV
				1:1	2/3	A1

8		7		6		5		4		3		2		1			
CUSTOMER DRAWING								REV.		DESCRIPTION		DESIGN		APPROVAL		DATE	

PCI Express Connectors Pinout

PIN #	Side B			Side A		
	Name	Description	Notes	Name	Description	Notes
1	+12V	+12V power		PRNST1#	Presense detect	
2	+12V	+12V power		+12V	+12V power	
3	+12V	+12V power		+12V	+12V power	
4	GND	Ground		GND	Ground	
5	SMBCLK	SMBus(System Management Bus) Clock		JTAG2	TCk(Test Clock),clock input for JTAG interface	
6	SMBDAT	SMBus(System Management Bus) Data		JTAG3	TDI(Test Data Input)	
7	GND	Ground		JTAG4	TDO(Test Data Output)	
8	+3.3V	+3.3V power		JTAG5	TMS(Test Mode Select)	
9	JTAG1	TRST#(Test Reset),resets the JTAG interface		+3.3V	+3.3V power	
10	+3.3Vaux	+3.3V auxiliary power		+3.3V	+3.3V power	
11	WAKE#	Signal for Link reactivation		PERST#	Fundamental reset	
Mechanical Key						
12	CLKREQ#	Clock Request Signal		GND	Ground	
13	GND	Ground		REFCLK+	Reference clock differential pair	
14	PETp0	Transmitter differential pair,Lane 0		REFCLK-		
15	PETn0			GND	Ground	
16	GND	Ground		PERp0	Receiver differential pair,Lane 0	
17	PRNST2#	Presence detect		PERn0		
18	GND	Ground		GND	Ground	
End of the X1 Connector				End of the X1 Connector		
19	PETp1	Transmitter differential pair,Lane 1		MFG	Manufacturer Test Mode	
20	PETn1			GND	Ground	
21	GND	Ground		PERp1	Receiver differential pair,Lane 1	
22	GND	Ground		PERn1		
23	PETp2	Transmitter differential pair,Lane 2		GND	Ground	
24	PETn2			GND	Ground	
25	GND	Ground		PERp2	Receiver differential pair,Lane 2	
26	GND	Ground		PERn2		
27	PETp3	Transmitter differential pair,Lane 3		GND	Ground	
28	PETn3			GND	Ground	
29	GND	Ground		PERp3	Receiver differential pair,Lane 3	
30	PWRBRK#	Emergency Power Reduction		PERn3		
31	PRNST2#	Presence detect		GND	Ground	
32	GND	Ground		RSVD	Reserved	
End of the X4 Connector				End of the X4 Connector		
33	PETp4	Transmitter differential pair,Lane 4		RSVD	Reserved	
34	PETn4			GND	Ground	
35	GND	Ground		PERp4	Receiver differential pair,Lane 4	
36	GND	Ground		PERn4		
37	PETp5	Transmitter differential pair,Lane 5		GND	Ground	
38	PETn5			GND	Ground	
39	GND	Ground		PERp5	Receiver differential pair,Lane 5	
40	GND	Ground		PERn5		
41	PETp6	Transmitter differential pair,Lane 6		GND	Ground	
42	PETn6			GND	Ground	
43	GND	Ground		PERp6	Receiver differential pair,Lane 6	
44	GND	Ground		PERn6		
45	PETp7	Transmitter differential pair,Lane 7		GND	Ground	
46	PETn7			GND	Ground	
47	GND	Ground		PERp7	Receiver differential pair,Lane 7	
48	PRNST2#	Presence detect		PERn7		
49	见续表	见续表		GND	Ground	

PIN #	Side B			Side A		
	Name	Description	Notes	Name	Description	Notes
49	GND	Ground	All The "GND" Pins Are Shorted	见上表	见上表	
50	PETp8	Transmitter differential pair,Lane 8		RSVD	Reserved	
51	PETn8			GND	Ground	All The "GND" Pins Are Shorted
52	GND	Ground		PERp8	Receiver differential pair,Lane 8	
53	GND	Ground		PERn8		
54	PETp9	Transmitter differential pair,Lane 9		GND	Ground	
55	PETn9			GND	Ground	All The "GND" Pins Are Shorted
56	GND	Ground		PERp9	Receiver differential pair,Lane 9	
57	GND	Ground		PERn9		
58	PETp10	Transmitter differential pair,Lane 10		GND	Ground	
59	PETn10		GND	Ground	All The "GND" Pins Are Shorted	
60	GND	Ground	PERp10	Receiver differential pair,Lane 10		
61	GND	Ground	PERn10			
62	PETp11	Transmitter differential pair,Lane 11	GND	Ground		
63	PETn11		GND	Ground	All The "GND" Pins Are Shorted	
64	GND	Ground	PERp11	Receiver differential pair,Lane 11		
65	GND	Ground	PERn11			
66	PETp12	Transmitter differential pair,Lane 12	GND	Ground		
67	PETn12		GND	Ground	All The "GND" Pins Are Shorted	
68	GND	Ground	PERp12	Receiver differential pair,Lane 12		
69	GND	Ground	PERn12			
70	PETp13	Transmitter differential pair,Lane 13	GND	Ground		
71	PETn13		GND	Ground	All The "GND" Pins Are Shorted	
72	GND	Ground	PERp13	Receiver differential pair,Lane 13		
73	GND	Ground	PERn13			
74	PETp14	Transmitter differential pair,Lane 14	GND	Ground		
75	PETn14		GND	Ground	All The "GND" Pins Are Shorted	
76	GND	Ground	PERp14	Receiver differential pair,Lane 14		
77	GND	Ground	PERn14			
78	PETp15	Transmitter differential pair,Lane 15	GND	Ground		
79	PETn15		GND	Ground	All The "GND" Pins Are Shorted	
80	GND	Ground	PERp15	Receiver differential pair,Lane 15		
81	PRNST2#	Presence detect	PERn15			
82	RSVD	Reserved	GND	Ground		

X.± 0.50	X.°± 3°	PART NO.: PIE164-3*11-000-0H	LUXSHARETECH			
.X± 0.25	.X°± 2°					
.XX± 0.20	.XX°± 1°	TITLE: PCIE SLOT PLUS 164PIN SMT TYPE CONNECTOR	SUPPLIER P/N:			
.XXX± 0.15	.XXX°± 0.5°					
UNITS:	mm	APPD: Rongzhe Guo 2026.07.10	DWG NO: PIE164-3511-000-0H-000			
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		DRAW: Mengmeng Zhao 2026.07.10		SCALE 1:1	SHEET 3/3	REV. A1

8

4

3

2

1

PIN #	Side B			Side A		
	Name	Description	Notes	Name	Description	Notes
1	+12V	+12V power		PRSNT1#	Presense detect	
2	+12V	+12V power		+12V	+12V power	
3	+12V	+12V power		+12V	+12V power	
4	GND	Ground		GND	Ground	
5	SMBCLK	SMBus(System Management Bus) Clock		JTAG2	TCK(Test Clock),clock input for JTAG interface	
6	SMBDAT	SMBus(System Management Bus) Data		JTAG3	TDI(Test Data Input)	
7	GND	Ground		JTAG4	TDO(Test Data Output)	
8	+3.3V	+3.3V power		JTAG5	TMS(Test Mode Select)	
9	JTAG1	TRST#(Test Reset),resets the JTAG interface		+3.3V	+3.3V power	
10	+3.3Vaux	+3.3V auxiliary power		+3.3V	+3.3V power	
11	WAKE#	Signal for Link reactivation		PERST#	Fundamental reset	
Mechanical Key						
12	CLKREQ#	Clock Request Signal		GND	Ground	
13	GND	Ground		REFCLK+	Reference clock differential pair	
14	PETp0	Transmitter differential pair,Lane 0		REFCLK-		
15	PETn0			GND	Ground	
16	GND	Ground		PERp0	Receiver differential pair,Lane 0	
17	PRSNT2#	Presence detect		PERn0		
18	GND	Ground		GND	Ground	
End of the X1 Connector				End of the X1 Connector		
19	PETp1	Transmitter differential pair,Lane 1	All The "GND" Pins Are Shorted	MFG	Manufacturer Test Mode	
20	PETn1			GND	Ground	
21	GND	Ground		PERp1	Receiver differential pair,Lane 1	
22	GND	Ground		PERn1		
23	PETp2	Transmitter differential pair,Lane 2		GND	Ground	
24	PETn2			GND	Ground	
25	GND	Ground		PERp2	Receiver differential pair,Lane 2	
26	GND	Ground		PERn2		
27	PETp3	Transmitter differential pair,Lane 3		GND	Ground	
28	PETn3			GND	Ground	
29	GND	Ground		PERp3	Receiver differential pair,Lane 3	
30	PWRBRK#	Emergency Power Reduction		PERn3		
31	PRSNT2#	Presence detect		GND	Ground	
32	GND	Ground		RSVD	Reserved	
End of the X4 Connector				End of the X4 Connector		
33	PETp4	Transmitter differential pair,Lane 4	All The "GND" Pins Are Shorted	RSVD	Reserved	
34	PETn4			GND	Ground	
35	GND	Ground		PERp4	Receiver differential pair,Lane 4	
36	GND	Ground		PERn4		
37	PETp5	Transmitter differential pair,Lane 5		GND	Ground	
38	PETn5			GND	Ground	
39	GND	Ground		PERp5	Receiver differential pair,Lane 5	
40	GND	Ground		PERn5		
41	PETp6	Transmitter differential pair,Lane 6		GND	Ground	
42	PETn6			GND	Ground	
43	GND	Ground		PERp6	Receiver differential pair,Lane 6	
44	GND	Ground		PERn6		
45	PETp7	Transmitter differential pair,Lane 7		GND	Ground	
46	PETn7			GND	Ground	
47	GND	Ground		PERp7	Receiver differential pair,Lane 7	
48	PRSNT2#	Presence detect		PERn7		
49	见续表	见续表		GND	Ground	

PIN #	Side B			Notes	Side A			Notes
	Name	Description			Name	Description		
49	GND	Ground	All The "GND" Pins Are Shorted	见上表	见上表		All The "GND" Pins Are Shorted	
50	PETp8	Transmitter differential pair,Lane 8		RSVD	Reserved			
51	PETn8			GND	Ground			
52	GND	Ground		PERp8	Receiver differential pair,Lane 8			
53	GND	Ground		PERn8				
54	PETp9	Transmitter differential pair,Lane 9		GND	Ground			
55	PETn9			GND	Ground			
56	GND	Ground		PERp9	Receiver differential pair,Lane 9			
57	GND	Ground		PERn9				
58	PETp10	Transmitter differential pair,Lane 10		GND	Ground			
59	PETn10			GND	Ground			
60	GND	Ground	All The "GND" Pins Are Shorted	PERp10	Receiver differential pair,Lane 10		All The "GND" Pins Are Shorted	
61	GND	Ground		PERn10				
62	PETp11	Transmitter differential pair,Lane 11		GND	Ground			
63	PETn11			GND	Ground			
64	GND	Ground		PERp11	Receiver differential pair,Lane 11			
65	GND	Ground		PERn11				
66	PETp12	Transmitter differential pair,Lane 12		GND	Ground			
67	PETn12			GND	Ground			
68	GND	Ground		PERp12	Receiver differential pair,Lane 12			
69	GND	Ground		PERn12				
70	PETp13	Transmitter differential pair,Lane 13		All The "GND" Pins Are Shorted	GND	Ground		
71	PETn13		GND		Ground			
72	GND	Ground	PERp13		Receiver differential pair,Lane 13			
73	GND	Ground	PERn13					
74	PETp14	Transmitter differential pair,Lane 14	GND		Ground			
75	PETn14		GND		Ground			
76	GND	Ground	PERp14		Receiver differential pair,Lane 14			
77	GND	Ground	PERn14					
78	PETp15	Transmitter differential pair,Lane 15	GND		Ground			
79	PETn15		GND		Ground			
80	GND	Ground	PERp15		Receiver differential pair,Lane 15			
81	PRSENT2#	Presence detect		PERn15				
82	RSVD	Reserved		GND	Ground			